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**** CONTINUING DATA *******

None Ah

**** FOREIGN APPLICATIONS *******

GERMANY 103 06 281.5 02/14/2003 *Ah*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>[Signature]</i> Examiner's Signature Initials	STATE OR COUNTRY GERMANY	SHEETS DRAWING 13	TOTAL CLAIMS 33	INDEPENDENT CLAIMS 4
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ADDRESS

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TITLE

Method of fabricating and architecture for vertical transistor cells and transistor-controlled memory cells

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